

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3174	(257/686).OCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2008/09/25 08:44
L2	584	memory and logic and 1	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2008/09/25 08:46
L3	12	"6580164"	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2008/09/25 11:15
L4	819	memory and logic and 257/777	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2008/09/25 11:39
L5	7	semiconductor adj chip and logic adj circuit and memory adj circuit and noise. clm.	US-PGPUB	OR	ON	2008/09/25 11:50
L6	1562	(257/e21.499). OCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2008/09/25 11:52

9/ 25/ 08 11:53:28 AM